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CONFIRMATION NO. 7212

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APPLICANTS

Thomas Zettler, Hohenkirchen-Siegersbrunn, GERMANY;

\*\* CONTINUING DATA \*\*\*\*\* *None*

\*\* FOREIGN APPLICATIONS \*\*\*\*\* *yes*  
 GERMANY 100 37 794.7 08/03/2000

IF REQUIRED, FOREIGN FILING LICENSE GRANTED  
 \*\* 09/10/2001

Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance <i>gmc</i>	STATE OR COUNTRY GERMANY	SHEETS DRAWING 13	TOTAL CLAIMS 28	INDEPENDENT CLAIMS 4
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Verified and Acknowledged  
 Examiner's Signature Initials

ADDRESS  
 24131  
 LERNER AND GREENBERG, PA  
 P O BOX 2480  
 HOLLYWOOD, FL  
 33022-2480

TITLE  
 Method and device for testing an integrated circuit, integrated circuit to be tested, and wafer with a large number of integrated circuits to be tested

FILING FEE  RECEIVED 1064	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees ( Filing ) <input type="checkbox"/> 1.17 Fees ( Processing Ext. of time ) <input type="checkbox"/> 1.18 Fees ( Issue )
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